



## Freeform Search

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<b>Database:</b>	US Pre-Grant Publication Full-Text Database
	<b>US Patents Full-Text Database</b>
	US OCR Full-Text Database
	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins

<b>Term:</b>	L36 and @py<=2003	 
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<b>Display:</b>	<input type="text" value="20"/> Documents in <b>Display Format:</b> <input type="text" value="TI"/> Starting with Number <input type="text" value="1"/>
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**Generate:** ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

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### Search History

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**DATE:** Thursday, May 25, 2006    [Printable Copy](#)    [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<u>L37</u>	L36 and @py<=2003	52	<u>L37</u>
<u>L36</u>	row adj address adj input\$1 same column adj address adj input\$1 same latch\$1	71	<u>L36</u>
<u>L35</u>	row adj address adj input\$1 same column adj address adj input\$1 same latch\$1	0	<u>L35</u>
<u>L34</u>	L33 and addressable adj latch	1	<u>L34</u>
<u>L33</u>	row adj address adj input\$1 same column adj address adj input\$1	486	<u>L33</u>
<u>L32</u>	l27 and (row or column) adj address adj input\$1	1	<u>L32</u>
<u>L31</u>	L27 same pixel\$1 same display\$1	2	<u>L31</u>
<u>L30</u>	L27 and row adj address adj input	1	<u>L30</u>
<u>L29</u>	L2 and row adj address adj input	0	<u>L29</u>
<u>L28</u>	addressable adj latch\$1 same (row or column) address adj input\$1	1	<u>L28</u>
<u>L27</u>	addressable adj latch\$1	318	<u>L27</u>
<u>L26</u>	L25 and @py<=2003	1076	<u>L26</u>
<u>L25</u>	pixel same coupl\$6 same (thin-film-transistor or TFT or thin adj film adj	2177	<u>L25</u>

	transistor)		
<a href="#">L24</a>	l21 and thin-film-transistor same (display adj device\$1 or pixel\$1)	6	<a href="#">L24</a>
<a href="#">L23</a>	l21 and thin-film-transistor adj (display adj device\$1 or pixel\$1)	0	<a href="#">L23</a>
<a href="#">L22</a>	l21 and thin-film-transistor adj (display adj device\$1 of pixel\$1)	0	<a href="#">L22</a>
<a href="#">L21</a>	L20 and @py<=2003	583	<a href="#">L21</a>
<a href="#">L20</a>	(display adj element or pixel) same coupled same (switch or transistor) same capacitor	1234	<a href="#">L20</a>
<a href="#">L19</a>	(display adj element or pixel) same coupled same (switch or transistor) same capacitor	0	<a href="#">L19</a>
<a href="#">L18</a>	(display adj element or pixel) same coupled same (switch or transistor)	4427	<a href="#">L18</a>
<a href="#">L17</a>	(latch or logic adj gate )same (switch or transistor) adj gate	2015	<a href="#">L17</a>
<a href="#">L16</a>	(latch or logic adj gate )same transistor adj gate	1784	<a href="#">L16</a>
<a href="#">L15</a>	address\$6 adj latch\$1 same input same (switch or transistor\$1) same pixel\$1	9	<a href="#">L15</a>
<a href="#">L14</a>	address\$6 adj latch\$1 same input same (switch or transistor\$1)	578	<a href="#">L14</a>
<a href="#">L13</a>	L12 and @py<=2003	52	<a href="#">L13</a>
<a href="#">L12</a>	(display or pixel\$1) same addressable adj latch	54	<a href="#">L12</a>
<a href="#">L11</a>	addressable adj latch	445	<a href="#">L11</a>
<a href="#">L10</a>	column adj address comparator same (display adj element\$1 or pixel\$1)	5	<a href="#">L10</a>
<a href="#">L9</a>	column adj address comparator	71	<a href="#">L9</a>
<a href="#">L8</a>	L7 column adj address comparator	0	<a href="#">L8</a>
<a href="#">L7</a>	row adj address adj comparator	118	<a href="#">L7</a>
<a href="#">L6</a>	L5 and pixel near 2 latch	6	<a href="#">L6</a>
<a href="#">L5</a>	pixel\$1 same latch	6043	<a href="#">L5</a>
<a href="#">L4</a>	pixel\$1 and addressble adj latch	0	<a href="#">L4</a>
<a href="#">L3</a>	display\$ and addressble adj latch	0	<a href="#">L3</a>
<a href="#">L2</a>	display adj element\$1 and addressble adj latch	0	<a href="#">L2</a>
<a href="#">L1</a>	display adj element\$1 same addressble adj latch	0	<a href="#">L1</a>

END OF SEARCH HISTORY

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**Generate:** ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

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Search

Clear

Interrupt

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### Search History

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**DATE:** Thursday, May 25, 2006   [Printable Copy](#)   [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L26</u>	L25 and @py<=2003	1076	<u>L26</u>
<u>L25</u>	pixel same coupl\$6 same (thin-film-transistor or TFT or thin adj film adj transistor)	2177	<u>L25</u>
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<u>L23</u>	l21 and thin-film-transistor adj (display adj device\$1 or pixel\$1)	0	<u>L23</u>
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<u>L21</u>	L20 and @py<=2003	583	<u>L21</u>
<u>L20</u>	(display adj element or pixel) same coupled same (switch or transistor) same capacitor	1234	<u>L20</u>
<u>L19</u>	(display adj element or pixel) same coupled same (switch or transistor) same capacitor	0	<u>L19</u>
<u>L18</u>	(display adj element or pixel) same coupled same (switch or transistor)	4427	<u>L18</u>
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	pixel\$1		
<u>L14</u>	address\$6 adj latch\$1 same input same (switch or transistor\$1)	578	<u>L14</u>
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<u>L2</u>	display adj element\$1 and addressble adj latch	0	<u>L2</u>
<u>L1</u>	display adj element\$1 same addressble adj latch	0	<u>L1</u>

END OF SEARCH HISTORY